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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known				
Application Number	09/828,049-Conf. #5766			
Filing Date	April 6, 2001			
First Named Inventor	Jason SOULOGLOU			
Art Unit	2191			
Examiner Name	C. C. Chow			
Attorney Docket Number	1801270.00126US1			

U.S. PATENT DOCUMENTS					
Examiner Cite Initials* No.	0:1-	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	AA*	US-5,930,509	07-27-1999	Yates et al.	
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	ВА	EP-0822485	02-04-1998	Sun Microsystems		

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²		
	CA	Goodwin D.W. et al. "Optimal and Near Optimal Global Register Allocation Using 0-1 Integer Programming." <i>Software Practice and Experience</i> . Wiley & Sons, Bognor Regis, gB. Vol. 26, No. 8. August 1996. Pages 929-965. XP000673808.			
	СВ	European Search Report for European Patent Application No. 03 02 0510 mailed July 19, 1007.			
	СС	Cifuentes C. et al. "Assembly to High-Level Language Translation." <i>IEEE</i> . November 1998. Pages 228-237.			
	CD	Chyi-Ren, Dow et al. "An Efficient Technique to Remove Transformations [Program Codes]." IEEE. December 1994. Pages 392-397.			
	CE	Girkar, M. et al. "Automatic Extraction of Functional Parallelism from Ordinary Programs." IEEE. Vol. 3, Issue 2. March 1992. Pages 166-178.			
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